

Appendix A

Amendments to the Abstract

Amendments to the Specification/Abstract:

Please amend the abstract as follows:

~~Methods and apparatuses for incremental, periodic storage of checkpoints in~~
~~a multi-threaded processor. A processor executes corresponding instruction threads as a~~
~~leading thread and a trailing thread. For a selected instruction, processor state~~
~~corresponding to the execution of the instruction is saved in a history buffer. This is~~
~~performed before writing a result from the selected instruction to a destination register. The~~
~~result from executing the selected instruction in the leading thread is compared to the result~~
~~from executing the selected instruction in the trailing thread. If the comparison indicates a~~
~~fault, then restoring the processor state corresponding to a previous instruction. Data from~~
~~the history buffer is used to perform the restoration.~~